

Registration Form

Name : _____
Designation : _____
Organization : _____
Address : _____

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e-mail ID : _____

Accommodation required ? : YES / NO
Registration is FREE
(for faculty members of AICTE recognized Institutions)

Signature of Participant

Sponsorship:

Prof. / Dr. / Mr. / Mrs. _____

is permitted to participate in the above program.

Place : _____
Date : _____

Approving/Sponsorship Authority
(Signature and Seal)

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AICTE Sponsored

Faculty Development Programme

On



15th - 27th July, 2013

Organized by

**Department of Electronics
and Communication Engineering**
CIT, Gubbi, Karnataka

in Association with



itie Knowledge Solutions



CHANNABASAVESHWARA INSTITUTE OF TECHNOLOGY

(ISO 9001:2008 Certified Institution)
Affiliated to VTU, Belgaum and Recognized by A.I.C.T.E New Delhi
N.H-206, (B.H-Road), Gubbi
TUMKUR - 572216, Karnataka

About the Workshop

Digital Signal Processing (DSP) is the mathematical manipulation of an information signal to modify or improve it. Designing and Developing Digital Signal Processing (DSP) applications is a complex task influenced by many parameters. But by using a systematic approach the result can be an efficient implementation.

The objectives of the programme are:

- To Understand Fundamentals of Signal Processing
- Design and Implementation of digital filters
- To explore application specific challenges in signal processing
- To understand differences between DSPs & other general purpose processors and explore DSP architectures
- To Choose DSP for an embedded application & Optimization and benchmarking techniques for DSP
- DSP tools – IDE and real-time application development techniques using DSPs
- Application specific case studies such as Biomedical Signal Processing, Control & Automation, Audio Processing, etc.
- To get hands on the Simulation Software for DSP design
- Selecting the hardware components required for the system

Course Topics

- Introduction to Digital Signal Processing
- Transforms - Z Transforms, DFT, DCT, Wavelet Transforms
- Introduction to digital filters – FIR and IIR
- Realisation of digital filters and its practical applications
- Digital Signal Processor Architecture
- Embedded System Development Life Cycle using DSP
- Embedded System Development - Overall Needs of the System
- Introduction to MATLAB® and DSP Toolbox®
- Audio Applications case study on TMS320C6713 / 6416
- Bio-Signal Acquisition in real time and analysis

About C.I.T

The **Channabasaveshwara Institute of Technology (CIT)** was established in the year 2001 and is affiliated to Visvesvaraya Technological University (VTU), Belgaum. It is located in an ideal and congenial environment on the outskirts of Gubbi, Tumkur city on the Bangalore - Honnavar National Highway 206, 90 km from Bangalore, spread over 60 acres of lush green, landscaped campus.

It is one of the educational institute among five run by Sree Channabasaveshwara Swamy Rural Education Society Regd. (CRED) Gubbi. The Chairman of the Society is Sri G. S. Basavaraj, Member of Parliament from Tumkur Constituency, Government of India.

At the time of inception, the college had a total intake of 180 students in the three undergraduate disciplines of Computer Science, Information Science and Electronics & Communication Engineering. In a short span of 10 years, the institute has grown significantly & has made a distinct name in the field of technical education. Today, the institute is offering 6 Undergraduate Programs, 4 Post Graduate Programs and 4 diploma programs with an annual intake of 900 students.

The college has the reputation of becoming an ISO certified Institute way back in 2004. CIT got ISO re-certification in 2007. Also, in the year 2009 CSE and ECE departments were accredited by National Board of Accreditation (NBA), New Delhi.

In 2010, CIT was rated one of the BEST ENGINEERING COLLEGES in India, by Careers 360, a popular magazine promoted by OUTLOOK group.

ECE Department

The Department of **Electronics and Communication Engineering** Started in the year 2001 and offers an UG course, B. E. in Electronics & Communication Engineering with an annual intake of 120 and a PG course M. Tech in Electronics with an intake of 18 students.

The department was started with a vision of providing quality education & to develop the technocrats. The department is committed to promote teaching, research, industrial interaction and multi dimensional development of the students with a balanced blend of theoretical as well as practical exposure by its group of highly qualified, dedicated and well experienced faculty members. The department has state-of-the-art laboratories & other infrastructural facilities. Many of the projects are sponsored from government organizations & a few patents have been filed from ECE department. The department also offers Vocational Training Programs (VTP) to impart practical exposure to students.

itie Knowledge Solutions

itie Knowledge Solutions is a Biomedical Product Development, Consultancy and Technology Education Company started by IITians. itie Knowledge Solutions focuses on providing cutting edge R&D, Software development, and training solutions to industries and educational institutions. Their project support includes both India & international operations.

The itie team is grouped with number of experts from industry and academia, enabling to deliver technology solutions in the areas of Embedded Technology, DSP, Bio-medical, Control engineering and automation to automotive, aerospace and biomedical industries.

Main mission of itie is to integrate the fundamental streams of engineering and information technology for the development of the novel business and knowledge solutions to meet the growing requirements of industries and educational organizations by constructing strong pools among them and by working in harmony with them.

Registration, TA, Accommodation & Transport

No Registration fee will be charged to the faculty members of AICTE recognized Institutions. Participants from Industry, Govt. departments have to pay Rs.1000/- per candidate as registration fee. (The Registration fee may be paid by a Demand Draft/ Cheque drawn in favour of **"The Principal", CIT, Gubbi.**) The faculty members from AICTE approved Engineering colleges and polytechnics are eligible to attend.

TA / DA will be paid as per existing norms (Sleeper class To & Fro fare by the shortest route). Boarding & Lodging facility will be Provided free of cost to out station participants.

Last date for Registration : 8th July, 2013.

Contact Details

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